Electronic Patent Application Fee Transmittal								
Application Number:	10	10707071						
Filing Date:	19	19-Nov-2003						
Title of Invention:		AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS						
First Named Inventor/Applicant Name:	D:	Darren L. Anand						
Filer:	St	Steven Fischman/Julie Yanopulos						
Attorney Docket Number:	В	BUR920030168US1						
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Utility Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:								
Claims:								
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Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
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Extension-of-Time:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Tota	150		